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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/527,820	05/11/2005	Yoshihiko Takagi	26642U	6449
20529	7590	02/12/2008		
NATH & ASSOCIATES 112 South West Street Alexandria, VA 22314			EXAMINER CERVETTI, DAVID GARCIA	
			ART UNIT 2136	PAPER NUMBER
			MAIL DATE 02/12/2008	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/527,820	Applicant(s) TAKAGI ET AL.	
	Examiner David Garcia Cervetti	Art Unit 2136	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 14 November 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 20-32 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 20-32 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 March 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>4/26/07, 5/11/05, 3/14/05</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 20-32 are pending and have been examined. Claims 1-19 have been cancelled.

Information Disclosure Statement

2. The information disclosure statement filed 5/11/05 fails to comply with 37 CFR 1.98(a)(2), which requires a legible copy of each cited foreign patent document; each non-patent literature publication or that portion which caused it to be listed; and all other information or that portion which caused it to be listed. It has been placed in the application file, but the information referred to therein has not been considered.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. **Claims 20-32 are rejected under 35 U.S.C. 102(b) as being anticipated by Iguchi et al (US Patent Application Publication 2002/0169960, hereinafter Iguchi).**

Regarding claims 20 and 31, Iguchi teaches

an access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of **(abstract)**:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of

data to access and designating an access area of the memory device, to the memory device (**fig. 19C, pars. 76-77, content access request includes address and size**);

the apparatus transmitting verification information generated based on the designation information, together with a processing command for the access area / verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area to the memory device (**pars. 121-122, verify request**);

the memory device verifying the verification information using the designation information / verification data using the designation information and the verification key (**pars. 121-124, verify certificate code**); and

the memory device executing the processing command when the verification succeeds (**pars. 77-78, execute command upon verification**).

Regarding claim 21, Iguchi teaches

an access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of (**abstract**):

the apparatus sharing a verification key with the memory device (**pars. 96-98, mobile terminal transmits session key to storage device**);

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device (**fig. 19C, pars. 76-77, content access request includes address and size**);

the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area (**pars. 121-122, verify request**);

the memory device verifying the verification data using the designation information and the verification key (**pars. 121-124, verify certificate code**); and

the memory device executing the processing command when the verification succeeds (**pars. 77-78, execute command upon verification**).

Regarding claims 22 and 32, Iguchi teaches

an access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of (**abstract**):

the apparatus sharing a verification key with the memory device (**pars. 96-98, mobile terminal transmits session key to storage device**);

the apparatus requesting transmission of random numbers to the memory device; the memory device transmitting random numbers to the apparatus (**pars. 75-78, generate random number**);

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device (**fig. 19C, pars. 76-77, content access request includes address and size**);

the apparatus transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command for the access area / the apparatus transmitting verification

data obtained by encrypting the designation information using a verification key, together with a processing command for the access area to the memory device (**pars. 121-122, verify request**);

the memory device verifying the verification data using the random numbers, the designation information and the verification key (**pars. 121-124, verify certificate code**); and

the memory device executing the processing command when the verification succeeds (**pars. 77-78, execute command upon verification**).

Regarding claim 23, Iguchi teaches

a memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising (**abstract**):

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write (**fig. 19C, pars. 76-77, content access request includes address and size**);

a designation information verifier that performs verification processing on the verification information using the designation information (**pars. 121-122, verify request**);

a storage area that stores data (**pars. 75-77, storage device**);

a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds (**pars. 77-78, execute command upon verification**);

a data transmitter that transmits data read by the storage area access section to the apparatus (**pars. 77-78, execute command upon verification**); and

a data receiver that receives data to write from the apparatus (**pars. 77-78, execute command upon verification**).

Regarding claim 27, Iguchi teaches

an information apparatus that reads and writes data of a specific area assigned to an area number from/in a memory device, the information apparatus comprising
(abstract):

a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area (**fig. 19C, pars. 76-77, content access request includes address and size**);

a verification information generator that performs processing for generating verification information based on the designation information (**pars. 121-122, verify request**);

a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a

processing command for reading or writing data (**pars. 77-78, execute command upon verification**);

a data transmitter that transmits data to the memory device when the processing command is write (**pars. 77-78, execute command upon verification**); and

a data receiver that receives data from the memory device when the processing command is read (**pars. 77-78, execute command upon verification**).

Regarding claims 24 and 28, Iguchi teaches wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to the apparatus / wherein the detection information verifier requests transmission of random numbers to the memory device and receives the random numbers from the memory device (**pars. 75-78, generate random number**).

Regarding claims 25 and 29, Iguchi teaches wherein the designation information verifier performs the verification processing using the verification information and a verification key / wherein the verification information generator performs the processing for generating the verification information using the designation information and a verification key (**pars. 75-78, verification using session key**).

Regarding claims 26 and 30, Iguchi teaches a verification key sharing section that shares the verification key with the apparatus / memory device (**pars. 96-98, mobile terminal transmits session key to storage device**).

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David García Cervetti whose telephone number is (571)272-5861. The examiner can normally be reached on Monday-Tuesday and Thursday-Friday.
6. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nasser Moazzami can be reached on (571)272-4195. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.
7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/David García Cervetti/